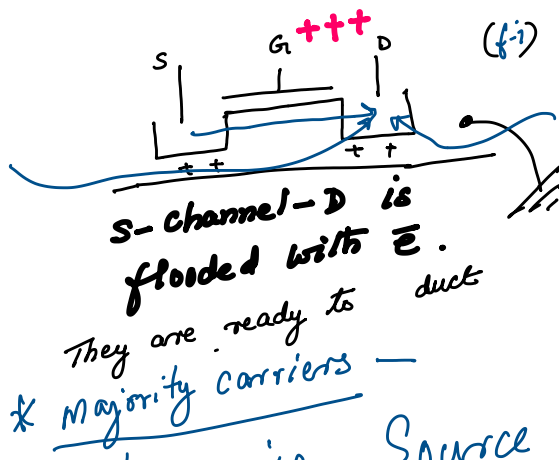
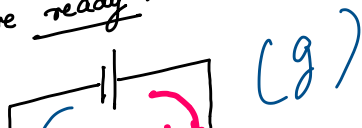
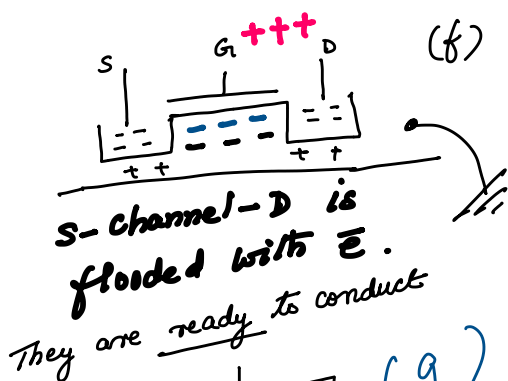
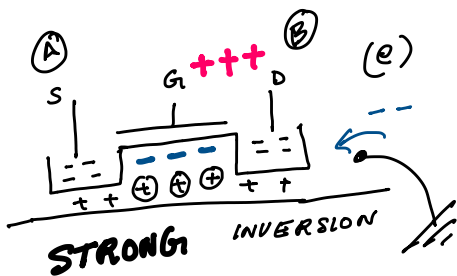
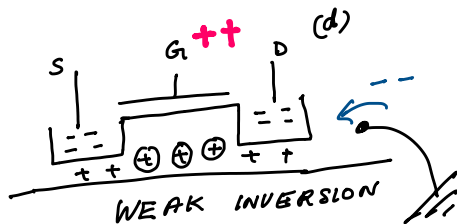
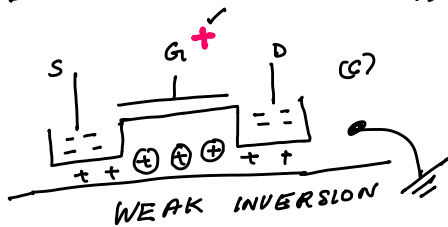
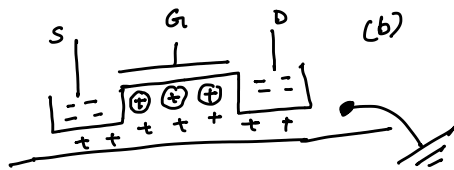
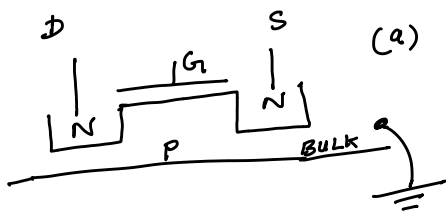
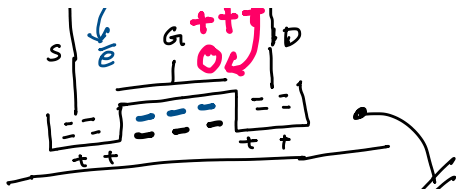


NMOS Operations Visualized

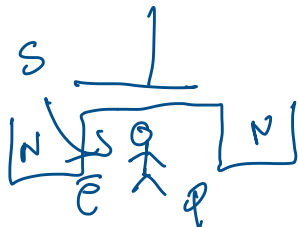
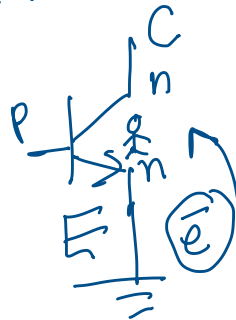
Tuesday, August 25, 2026 5:12 PM



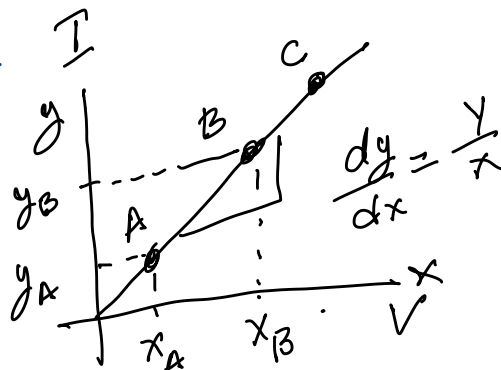
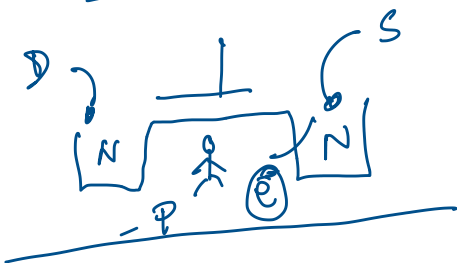


enters via source
exits via Drain

* What determines which pin is the source, drain?

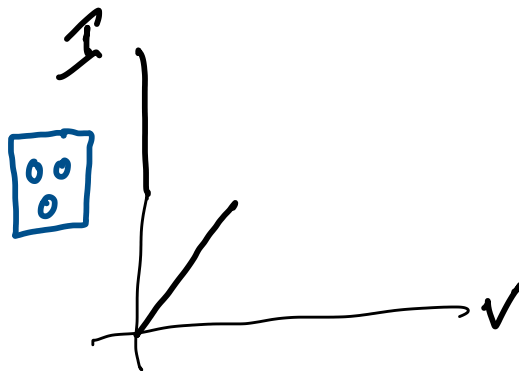
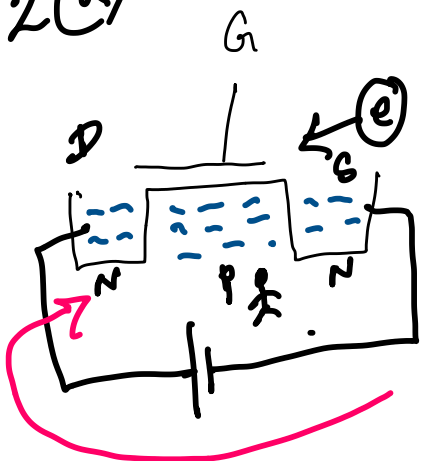


CD 4007



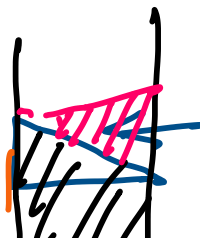
Resistor

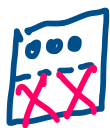
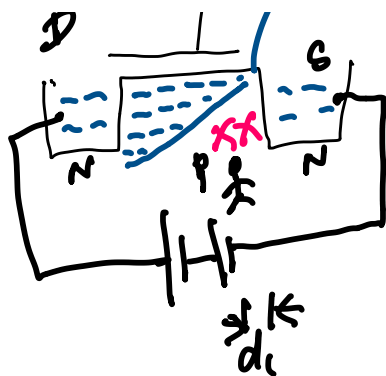
2(a)



2(b)

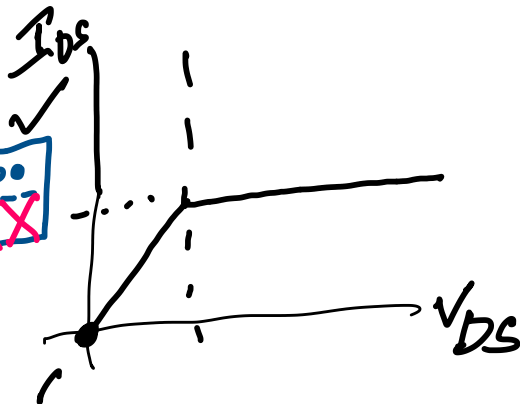
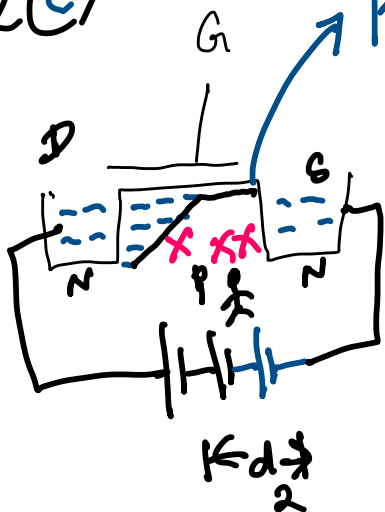
pinch off





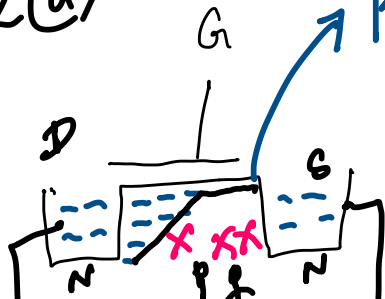
water moves as train acceleration

2(c) pinch off

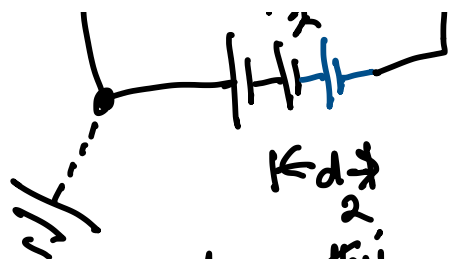


fig(2c) $d_2 > d_1$ fig(2b)

2(d) pinch off



saturated



Visualize this
point as a
reference

